

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: James P. BAUKUS, et al.)	Re: Preliminary Amendment
)	
Serial No.: not yet assigned)	
Filed: concurrently herewith)	Group: 2815
)	
Divisional of USSN <u>09/882,892</u>)	Examiner: Joseph Nguyen
filed on <u>June 15, 2001</u>)	
)	
For: "A PERMANENTLY ON TRANSISTOR)	Our Ref: B-3948DIV 621654-4
IMPLEMENTED USING A DOUBLE)	
POLYSILICON LAYER CMOS PROCESS)	
WITH BURIED CONTACT")	Date: February 26, 2004

PRELIMINARY AMENDMENT

MAIL STOP PATENT APPLICATION
Commissioner for Patents
P.O. BOX 1450
Alexandria, VA 22313-1450

Sir:

The above-identified application is a divisional of pending prior U.S. Patent Application Serial No. 09/882,892 filed on June 15, 2001. This paper is being filed as a Preliminary Amendment to the above-identified Application being filed concurrently herewith. Prior to the examination of the above-identified Application, it is respectfully requested that the amendments presented below be made to the Application.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims begin on page 3 of this paper.

Amendments to the Abstract begin on page 5 of this paper.

Remarks begin on page 6 of this paper.